

भारतीय सूचना प्रौद्योगिकी संस्थान, नागपुर Indian Institute of Information Technology, Nagpur

"An Institution of National Importance by an Act of Parliament"

Survey No. 140,141/1 Behind Br. Sheshrao Wankhade Shetkari Soot Girni, Village: Waranga, Po: Dongargaon (Butibori), District : Nagpur (Maharashtra) -441108

Website:www.iiitn.ac.in Email:director@iiitn.ac.in, registrar@iiitn.ac.in Phone:9405215010

IIITN/ADMIN/PhD_2024-25/001

Date: 6th Feb 2025

<u>PhD ADMISSION NOTIFICATION 2024-25</u> Under Visvesvaraya Full-time PhD Scheme for Electronics and IT: Phase II

Indian Institute of Information Technology, Nagpur (IIITN) is one of the 20 Indian Institutes of Information Technology established under the Public-Private Partnership Scheme by the Ministry of Education (MoE), Government of India. IIITN has been declared as an "Institution of National Importance" and at present, the Institute is functioning under the Department of Higher Education, Ministry of Human Education and is supported by the Department of Higher Education, Government of Maharashtra and TataConsultancy Services, Mumbai as Industry Partner.

IIITN is offering Under Graduate and PhD Programmes in Computer Science & Engineering (CSE) and Electronics & Communication Engineering (ECE) Courses.

The Institute is located at Survey No. 140,141/1 Behind Br. Sheshrao Wankhade Shetkari Soot Girni, Village: Waranga, Po: Dongargaon (Butibori), District: Nagpur (Maharashtra) - 441108. The development of the state-of-the-art Permanent Campus of the Institute is being undertaken on sprawling 100 acres of land.

The Institute invites applications from eligible Indian Nationals for admission to the Doctor of Philosophy (PhD) Programme expected to start from 3rd March 2025. The admission to PhD is offered under Full-time Visvesvaraya PhD Scheme for Electronics and IT: Phase II

<u>About Programme</u>

Full-time PhD program 2024-25 under "Visvesvaraya PhD Scheme for Electronics and IT: Phase-II". Ministry of Electronics and Information Technology, Government of India initiated "Visvesvaraya PhD Scheme for Electronics and IT" with an objective to enhance the number of PhD's in Electronics System Design & Manufacturing (ESDM) and IT/IT Enabled Services (IT/ITES) sectors in the country. This is an extremely competitive national level PhD fellowship scheme under the National Policy on

Electronics, Govt. of India for motivated students in the upcoming state-of-art Electronic System Design and Manufacturing (ESDM) and IT / IT Enabled services (IETS) sector research areas.

Doctor of Philosophy (PhD) Programme under Full-time Visvesvaraya PhD scheme for Electronics and IT identifies the following thrust areas of research.

Department	Research Areas
IT/IT Enabled Services (IT/ITES)	All areas in Computer Science & Engineering like Operating Systems, Databases, Computer Networking, Compilers, Computer Graphics, Software Engineering, Image Processing and Computer Vision, Big Data, Data Mining, Artificial Intelligence, Machine Learning, Deep Learning, Natural Language Processing, Computational Biology, Internet of Things, Information Security, RTOS and Embedded System and other IT Enabled Services.
Electronics System Design & Manufacturing (ESDM)	All areas in Electronics System Design & Manufacturing (ESDM) and Communication Engineering like Communication, Wireless or RF Communications, Microwave Engineering, Antennas, 5G, LTE, MIMO Technologies, Microprocessors and Embedded Systems, Image and Video Signal Processing, Computer Vision, Instrumentation, Opto-electronics, VLSI Design, Internet of Things, Automotive Electronics, Mechatronics, Embedded System Design, Wireless Sensor Network, Cognitive Radio, LI-FI, and other Advanced topics of Electronics and Communication Engineering.

Eligibility Criteria:

Essential: Master's Degree in Engineering/Technology with Bachelor's in Engineering/Technology in ECE/IT/CSE allied disciplines with First Class or 60% marks (55% marks for SC/ST/PwD) / CPI or CGPA greater than 6.75 (6 for SC/ST/PwD) on a 10-point scale.

Desirable: A valid GATE Score.

Scholarship details: The Full-time PhD candidates under Phase II of Visvesvaraya PhD Scheme for Electronics and IT for Academic Year 2024-25 are eligible to receive the fellowship as Rs. 38750/-per month for initial 2 years and Rs.43750/-per month 3rd year onwards for a period of 5 years with Research Contingency Grant as applicable.

1. <u>Fees Structure:</u>

The payment of Annual Fees may be done on a semester basis as under:

Odd Semester (Ex. 1 st , 3 rd etc.)	In the month of February for the year 2025. In the month of January from 2026 onwards	
Even Semester (2 nd , 4 th etc.)	In the month of June	

		Amount (in Rs)		
Sr. No.	Particulars	Odd Semester	Even Semester	Total Annual Fees
1	Tuition Fee	35000	35000	70000
2	Development Fee	6000	6000	12000
3	Registration Fee	1500	1500	3000
4	Library Fee	2000	2000	4000
5	Internet Charges	1000	1000	2000
6	Infrastructure Usage Fee	2000	2000	4000
7	Welfare Fee	1250	1250	2500
8	Gymkhana Activity & Magazine Fee	1250	1250	2500
	Total Fees =	50,000/-	50,000/-	1,00,000/-

The Semester Wise Fees Structure for all the categories of candidates is as under:

2. <u>Application Procedure for Programme:</u>

1. The applications are to be filled in online mode with all relevant documents attached. **To avail the online form a Gmail account is essential.**

Link for Online Prescribed Application Form: https://forms.gle/EbnR9RRhz6JkuHBZ7

- 2. Online application form without relevant supporting enclosures/submitted after the last date/ without application fees will be outrightly rejected. No correspondence shall be entertained in this regard. Interim correspondence will also not be entertained or replied to.
- 3. The last date for online submission of the application is 17/02/2025 up to 17:00Hrs.
- 4. Application Fee: The Application Fee shall be as under:

Sr. No.	Particulars	Application Fees	GST	Total Fees
1	General/ OBC-NCL Candidates/EWS	Rs. 1000/-	Rs. 180/-	Rs. 1180/-
2	SC/ST/PwD Candidates	Rs. 500/-	Rs .90/-	Rs. 590/-

5. The Application Fee shall be payable through Demand Draft drawn in favor of Director, IIIT, Nagpur payable at Nagpur or by UPI/NEFT/RTGS in the following Bank Account (Attach Receipt with online submitted application):

Account Name	IIITNGENERAL
Account Number	41759739179
Name of the Bank	State Bank of India, VRCE Branch, South Ambazari Road,
	VNIT, Nagpur
Bank Code	06702
IFSC Code	SBIN0006702
MICR Code	440002005

- 6. Candidates are required to write his/her name and contact number on the UPI/NEFT/RTGS Receipt.
- 7. The Application Fee is non-refundable.
- 8. No TA/DA and/or Lodging/Boarding will be paid for attending the Written Test / Interview. The Institute may arrange for accommodation in its Guest House ON SHARED & CHARGEABLE BASIS subject to availability and "First Come First" basis

for attending the Written Test / Interview. The details of the Guest House Charges are available on the website of the Institute. For booking of the Guest House the candidates may send an email on <u>aosd@iiitn.ac.in.</u>

9. Any corrigendum /changes /updates shall be made available only on the Institute's Website viz. <u>www.iiitn.ac.in</u>. The candidates are advised to keep on watching the Institute's Website for any information/updates on the PhD Admission Process.

3. Selection Process:

The PhD Admissions shall be based on Written Test/Computer Based Test (CBT) and Offline Interview.

The procedure shall be as follows:

- **<u>Stage1</u>: Written/CBT-** Eligible candidates are required to appear for the Written/Computer Based Test.
- **Qualifying Score in Written / CBT:** The minimum qualifying score shall be a normalized score decided taking into account the number of candidates to be called forinterview. But in no case, it will be less than 40%. The normalization will be done by considering the top score to be 100% and scaling the scores of other candidates accordingly.
- <u>Stage 2:</u> Interview Candidates qualifying the Written / CBT shall be eligible to appear for a Personal Interview.
- <u>Stage 3:</u> Calculation of Combined Score A Combined Score shall be calculated based on the score in Written/CBT, Score in Personal Interview and GATE Qualification as under:

		Weightage Distribution		
Candidate Category	Total Marks	Written/ CBT	Offline Interview	GATE Qualification*
For All Candidates	100	40	50	10

*Candidates with GATE Qualification shall be obtaining the allotted marks. The Final Merit List shall be prepared based on Combined Score.

4. Examination Pattern:

a. Written/Computer Based Test:

The question paper for the written/CBT shall comprise of two sections.

<u>Section-1</u> would comprise of questions on General Ability and Communication Skills.

<u>Section-2</u> shall have questions based on the syllabus for all respective categories exceptCandidates from Industry category as under:

Stream	Syllabus		
Electronics and Communication Engineering/ Electronics System Design & Manufacturing (ESDM)	Engineering Mathematics, Networks, Signals and Systems, Electronic Devices, Analog Circuits, Digital Circuits, Control Systems, Communications, Electromagnetic.		
Computer Science and Engineering and IT/IT Enabled Services (IT/ITES)	Engineering Mathematics, Digital Logic, Computer Organization and Architecture, Programming and Data Structures, Algorithms, Theory of Computation, CompilerDesign, Operating System, Data bases, Computer Networks.		

b. Submission of Statement of Purpose (SoP):

All the candidates are required to attach a Statement of Purpose (SoP) in the format available in Annexure–1 to the online soft copy application.

5. <u>Tentative Admission Schedule:</u>

Sr. No.	Particulars	Tentative Dates
1	Advertisement and availability of application forms	6 th Feb 2025
2	Last date for submission of completed application forms	17 th Feb 2025 [online]
3	Display of list of eligible candidates for Written/CBT	20 th Feb 2025
4	Written/CBT and display of the list of candidates qualified for interview.	25 th Feb 2025
5	Online/Offline Interview	25 th Feb 2025
6	Declaration of Results	27 th Feb 2025
7	Payment of PhD fees on or before	28 th Feb 2025
8	Registration	28 th Feb 2025
9	Semester Begins	3 rd March 2025

6. <u>Submission of Documents/Certificates while reporting to institute:</u>

The <u>self-attested copies</u> of the following documents are required to be attached with the hardcopy Application Form and scanned copies of the same to be attached with online forms should be brought for verification if shortlisted failing which the application shall not be evaluated and shall stand summarily rejected:

- 1. The hard copy of the Online Prescribed Application Form is duly signed on all the pages.
- 2. 10th Standard Certificate.
- 3. 12th Standard Certificate/Diploma Certificate.
- 4. Graduation Mark-Sheets for all years.
- 5. Graduation Degree Certificate.
- 6. Master's Degree & Mark-Sheets for all semesters.
- 7. Experience /Service Certificate for all the previous employments (if in period of five years).
- 8. EWS and OBC-NCL Certificate (wherever applicable) issued in the currentFinancial Year only as per Central Government Format.
- 9. Cast Validity Certificate for SC/ST/OBC-NCL candidates belonging to the State of Maharashtra.
- 10. SC/ST/PwD Certificates (wherever applicable) in prescribed pro-forma of Central Government Only.
- 11. Scanned Copy of NEFT payment receipt or scanned copy of DD for online formand actual DD or original NEFT receipt for hard copy form.

Original Documents and Id Proof would have to be produced during the Documentation Verification Process to be undertaken on the date of Interview failing which the Candidate may not be allowed to appear for the Selection Process.

7. Minimum Exit Criteria:

All the registered PhD students need to complete the requirement of coursework of 6 credits by M. Tech Degree Holders.

- a. Courses offered by the Institute.
- b. Credits can be earned through successful completion of the online courses availableon MOOC platforms like NPTEL, MIT-OCW, Coursera, edX, etc (The total minimum course hours for an online course should be 40 hours/12 weeks.

Publications:

The minimum requirement for submitting the thesis is publication in 1 SCI/SCIE indexed journal **and** 1 SCI/SCIE/Scopus index journal.

8. <u>General Terms & Conditions:</u>

1. M.Tech final year appeared candidates are also eligible to apply under a suitable category provided that the candidate must submit the M.Tech provisional degree on the declaration of results.

- 2. In case of any dispute regarding the PhD Admission Process, the place of jurisdiction shall be Nagpur, Maharashtra (India) only.
- 3. The decision of the Director, IIITN shall be final in all the matters related to Admission Process.
- 4. The Institute reserves its right to cancel/postpone/hold the Admission Process at any stage without citing any reason thereof.
- 5. Mere eligibility doesn't ensure selection. The Institute reserves the right to select a limited number of PhD candidates in each of the departments at the sole discretion of the Director, IIITN.
- 6. The list of short-listed candidates shall be displayed on the Institute's Website. Candidates are advised to visit the IIITN website <u>www.iiitn.ac.in</u> regularly. No correspondence regarding reasons for not shortlisting the candidate shall be entertained.
- 7. The candidates are required to have at least one working email and mobile number which will be used during the entire admission process for communication, if required.
- 8. In case of any inadvertent mistake in the process of admission detected at any stage even after the registration of the candidate for PhD Programme, the Institute reserves the right to modify/withdraw/cancel any communication made to the candidates.
- 9. For more details regarding the six-monthly seminar and other Academic Rules related to PhD Program, candidates can refer to the Academic Rulebook available on the Institute Website viz.<u>www.iiitn.ac.in</u>.
- 10. Canvassing in any form shall lead to disqualification of the Candidate for admission.
- 11. Clarification or queries in the context of the Admission Process may be sent on the email id viz.<u>phdcell@iiitn.ac.in.</u>

Place: Nagpur Date: 06/02/2025

DIRECTOR

Annexure – 1

STATEMENT OF PURPOSE (SoP)

Statement of Purpose (SoP) is an opportunity to share with the Admission Committee your thoughts and feelings about Postgraduate Studies at IIIT Nagpur including your preparation for the same.

Briefly describe past project / research work done by you. Restrict yourself to 500 to 600 words.

The personal SoP will aid the Admission Committee in evaluating your application.

- 1) If you are applying for more than one Academic Discipline you may include separate SoP for each discipline.
- 2) The proposal should contain:
 - \Rightarrow problem identification / research area,
 - \Rightarrow brief review of literature, and
 - \Rightarrow methodology

Name:

PhD Programme in _____