

Brief Profile

Dr. OMPRAKASH GOPAL KAKDE has obtained B.E.(Electrical Engg.) degree in 1986 from Visvesvaraya National Institute of Technology(VNIT), (Formerly VRCE), Nagpur, Nagpur University, M.Tech.(Computer Science & Engg.) degree in 1988 from IIT Bombay, Powai Mumbai, and Ph.D. degree in 2004 from Visvesvaraya National Institute of Technology(VNIT), Nagpur University with Title of Thesis “Exploring Parallelism in Compilation Process”. He also holds M.A.(Public Administration) degree of Nagpur University.

Dr. kakde has worked as **Director** of VJTI, Mumbai, one of most reputed and more than 130 years old Autonomous Institution fully funded by Government of Maharashtra from 1st June 2012 to 31st May 2017(5 Years). He was Head of Electronics & Computer Science Department at VNIT for 3 years, Nagpur, and Dean (Research & Consultancy) at VNIT, Nagpur for 3 years. Total experience academic, research guidance and administrative is 29 years and 6 months as on 1st July. 2018.

Dr. kakde has guided 06 PhDs till completion and 02 scholars are currently working under him. He has Published 12 research papers in International Journals, 17 in International conferences, and 02 in National Journals. He has also published 06 books which includes books on Compiler Design, Theory of Computation, Data Structures & Algorithms in USA and India. He is member of IE, CSI, ACM.

Recognitions:

Organizing committee for international conference CSI RAM-2006, Bangkok	Reviewer for International Conference held between 7 June 2006 to 9 June 2006
World Bank	TEQIP-II Coordinator at VNIT, Nagpur
IET UK	Accreditor
National Board of Accreditation(NBA)	Member of Engineering Evaluation and Accreditation committee for Tier –I Institutes, since 2013. Working as Chairman of visiting team for Tier-II, Institutes. Worked as member of committees for SAR preparation. Worked as member of program committee for WOSA-2016.
AICTE	Member of western regional Committee from 2014 to 2017. Worked as chairman of standing Appellate Committee.
Govt. of Maharashtra	Member of Shikhan Shulka Samitee since Dec. 2012 to April 2016, which is reconstituted and renamed under act as Fee Regulatory Authority of Govt. of Maharashtra

National Institute of Industrial Engineering(NITIE)	Member of Academic Council, since 2014
Western Regional Instrumentation Center of Mumbai University	Member of Governing Council, from 2013 to 2016.
Babasaheb Ambedkar Technological University (BATU) Lonere	Chancellor's nominee on Executive Council, from 2013 to 2016.
Amravati University	Member, RRC (Research Recognition Committee), from 2011 to 2016.
Nagpur University	Chairman, BOS (IT) from 2001 to 2002
Chattisgarh Swami Vivekanand Technological University(CSVTU)	Member of Doctoral Research Committee from 2006 to 2008
BoG, VJTI, Mumbai	Invitee member
BoG, BVM College of Engineering, Vallabh, Vidyanagar, Gujrat	Member

Details of Books authored:

Title of Books	Discipline	Referred for study in Higher Education at the National/International level
Algorithms for Compiler Design Charles Rivers Inc. USA.	Computer Science & Engineering	<ul style="list-style-type: none"> •Wentworth Institute of Technology, Boston , Massachusetts •University of Sydney
Comprehensive Compiler Design Laxmi Publications New Delhi.	Computer Science & Engineering	<ul style="list-style-type: none"> •Kerala University •Swami Ramanand Tirth University, Nanded
C & Data Structures Charles Rivers Inc. USA.	Computer Science & Engineering	
C & Data Structures Willey-DreamTech. Delhi	Computer Science & Engineering	Indraprastha University, Delhi, AMIETE, Calicut University,
Theory of Computation Laxmi Publications New Delhi.	Computer Science & Engineering	Reference Book in Mumbai University
Data Structures and Algorithms ISTE, New Delhi	Computer Science & Engineering	West Bengal Technological university